IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Min-sang Park et al. Serial No.: To Be Assigned Filed: Concurrently Herewith

For: INTEGRATED CIRCUIT DEVICES HAVING DATA INVERSION CIRCUITS THEREIN

WITH MULTI-BIT PREFETCH STRUCTURES AND METHODS OF OPERATING SAME

Date: April 1, 2004

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449. Item 1 was listed on the PTO-1449 was cited in parent application Serial No. 10/397,773, filed March 26, 2003. As the benefit of this application is claimed under 35 U.S.C. §120, no copies need to be furnished in accordance with 37 C.F.R. §1.98(d); however, copies will be furnished on request. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. §1.56 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted

Myers Bigel Sibley & Sajovec, P.A. P. O. Box 37428

Raleigh, North Carolina 27627 Telephone: (919) 854-1400 Facsimile: (919) 854-1401

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APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

andi L. Riggs

FORM PTO		U.S. Department ent and Trademark		Attorney Docket Number 5649-11001P			Serial No. To Be Assigned	
LIST OF DOCUMENTS CITED BY APPLICANT							İ	rissigned
(Use several sheets if necessary)								
					Applicants: Min-sang Park et al.			
					Filing Date: Concurrently Herewith		Group	
U. S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date if Appropriate
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FOREIGN PATENT DOCUMENTS								
		Document						Translation
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								I
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